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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/942,638	08/31/2001		Woon Jin Jung	P-0239	8774
34610	7590	12/29/2004		EXAM	INER
FLESHNE	R & KIM	I, LLP	PHU, PHUONG M		
P.O. BOX 221200 CHANTILLY, VA 20153				ART UNIT	PAPER NUMBER
				2631	
				DATE MAILED: 12/29/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/942,638	JUNG, WOON JIN				
Office Action Summary	Examiner	Art Unit				
	Phuong Phu	2631				
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet wit	th the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, and - If NO period for reply is specified above, the maximum statutory perion  - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the may earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a re- reply within the statutory minimum of thirty od will apply and will expire SIX (6) MON- tute, cause the application to become AB.	eply be timely filed  (30) days will be considered timely.  FHS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on 31	August 2001.					
	his action is non-final.					
3) Since this application is in condition for allow	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) <u>1-10</u> is/are pending in the application 4a) Of the above claim(s) is/are withd 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1-4</u> is/are rejected. 7) ⊠ Claim(s) <u>5-10</u> is/are objected to. 8) □ Claim(s) are subject to restriction and	rawn from consideration.					
Application Papers		•				
9)☐ The specification is objected to by the Exami	iner.					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the	* ' '					
Replacement drawing sheet(s) including the corn						
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority docume</li> <li>2. Certified copies of the priority docume</li> <li>3. Copies of the certified copies of the priority application from the International Bure</li> <li>* See the attached detailed Office action for a literal</li> </ul>	ents have been received. ents have been received in Apriority documents have been eau (PCT Rule 17.2(a)).	oplication No received in this National Stage				
Amort months						
Attachment(s)  1) Notice of References Cited (PTO-892)	4) Interview S	ummary (PTO-413)				
<ul> <li>Notice of References Cited (PTO-032)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/(Paper No(s)/Mail Date <u>8/31/01</u>.</li> </ul>	Paper No(s	//Mail Date formal Patent Application (PTO-152)				

#### **DETAILED ACTION**

#### **Priority**

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### Claim Objections

2. Claim 2 recites the limitation "a difference value between the amount of stored data in the elastic store and an amount of data outputted from the elastic store, less a value of the digital signal wave". It is unclear what the phrase "less a value of the digital signal wave" means.

Correction is required.

## Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Reilly et al (5,311,511).

-As per claim 1, see figures 3, 5 and 11 and col. 5, line 63 to col. 7, line 17, col. 10, line 47 to col. 11, line 18, Reilly et al discloses a system (see figure 3) comprising:

an elastic store (1) that stores data (SONET DATA IN) received from a digital network (SONET), (equivalent with the limitation "a synchronous digital hierarchy (SDH) network");

a generator means (12, 9), (equivalent w

a generator means (12, 9), (equivalent with "pattern generator"), of controlling a data read speed (DSCLK) so the elastic store maintains a constant amount of stored data (see figure 1, and col. 6, lines 14-28);

a sequencer (DDS REFERENCE CLOCK (see figure 5) or REFERENCE CLOCK (see figure 11), (equivalent with "modulation sequencer"), of generating signals (CLOCK) representative of a digital signal wave having a constant period (see col. 6, lines 65-68) and a amplitude; and

a controlling means (7, 10) (see figure 3), (equivalent with "phase level detector"), of controlling the generator means using the digital signal wave (see also figure 5 or figure 11).

-As per claim 2, Reilly et al discloses that the controlling means controls the generator means using a signal (OFFSET) (see figure 3) indicating a difference value between the amount of stored data in the elastic store and an amount of data outputted from the elastic store, and a timing value of the digital signal wave (see figure 5 or figure 11).

-As per claim 3, Reilly et al discloses that the controlling means is operated at time intervals based on the digital signal wave upon receipt of the digital signal wave from the sequencer (see figure 5 or figure 11).

-As per claim 4, Reilly et al discloses that the generator means performs a controlling operation of a pattern generation speed of the data read speed (DSCLK) at regular time intervals based on the timing value of the digital signal wave (see figures 3, 5 and 11).

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# Allowable Subject Matter

5. Claims 5-10 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong Phu whose telephone number is 571-272-3009. The examiner can normally be reached on M-F (6:30-2:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on 571-272-3021. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phuong Phu Primary Examiner Art Unit 2631

Phung phu

Phuong Phu 12/01/04 PRIMARY EXAMINED